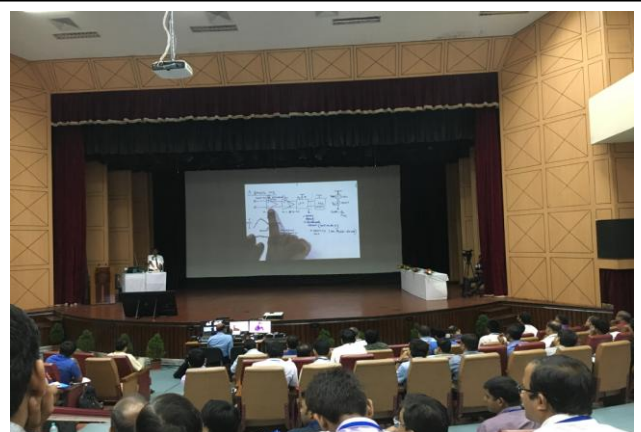


**ONE WEEK ISTE STTP ON "CMOS, MIXED-SIGNAL AND RADIO  
FREQUENCY VLSI DESIGN" FROM 19 SEPT TO 23 SEPT 2016 AT IIT  
KHARAGPUR**

One week ISTE STTP on “**CMOS, Mixed-Signal and Radio Frequency VLSI Design**” under “**T10KT**” program of NMEICT was held from 19 Sept to 23 Sept 2016 at **IIT Kharagpur**, West Bengal. **Ms. Seema (Assistant Professor, ECE)** from Dronacharya College of Engineering, Gurgaon (RC ID - 1299) attended this programme. An important initiative has been taken by IIT Bombay and IIT Kharagpur to work with Engineering Colleges of India to enhance the teaching skills of faculty colleagues in core Engineering and Science subjects by conducting ISTE Short Term Training Programme (STTP) under Train Ten Thousand Teachers (T10KT) project using 353 established remote centres across India.



*Ms. Seema receiving the certificate from Prof. Raja Datta*



*Prof. Mrigank Sharad delivering his lecture*